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Isolated Bidirectional DC–DC Converter for SuperCapacitor Applications

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Abstract. This paper proposes a new bidirectional DC/DC converter for supercapacitor applications. The proposed converter has a parallel structure in supercapacitor side (where voltage is low and current is high) and a series structure in the other side. This structure increases efficiency of the converter. For current sharing in the parallel side of the proposed converter, two different methods are recommended and compared in this paper: Current balancing transformer (CBT) and two separate inductors (TSI). Simulation and experimental results show performance of the proposed converter.

Key words

Current sharing, parallel primary, bidirectional converter, supercapacitor, fuel cell.

1. Introduction

Currently fuel cell electric vehicles (FCEV) are considered as an attractive option for future cars because of environmental issues and alternative energy requirements. However since fuel cell stack has a slow response, using an auxiliary energy storage device such as battery or supercapacitor (SC) is recommended in the fuel cell (FC) applications [1-3]. While the battery has a large energy density and SC has a high power density, FC-Battery hybrid and FC-SC hybrid systems offer different features. However, FC-SC-Battery hybrid systems in Fig. 1 have been shown to have superior features [2-3].

Because of charge dependent voltage of SC, a bidirectional DC/DC converter is needed for bidirectional power

exchange between SC and other parts of the system for different voltage levels [2-4]. Isolated full-bridge converter in Fig. 2 is a common DC/DC converter topology [5-6]. For high power applications, parallel isolated full-bridge converters have been proposed [7]. In fuel cell applications, generally low voltage is required to be boosted to higher voltages. Fig. 3 shows the primary parallel isolated boost converter proposed in [8] which is suitable for high voltage gain applications. This converter is composed of full-bridge stages with parallel primary connections (where current is high and voltage is low) and a single rectification stage with series secondary connection (where current is low and voltage is high). Current sharing is ensured by the series connection of transformer secondary windings and small cascaded current balancing transformer (CBT) on the primary side.

In this paper, the unidirectional converter presented in [8] is modified to handle bidirectional power flow in energy storage applications. For this purpose, the diode bridge rectifier on the secondary side has been replaced with a full bridge inverter. In addition a detailed analysis has been carried out comparing two different current balancing configurations. Using two separate inductors (TSI) instead of current balancing transformer (CBT) is recommended due to cost and simplicity. It has been shown that the current sharing performance is similar in both cases.

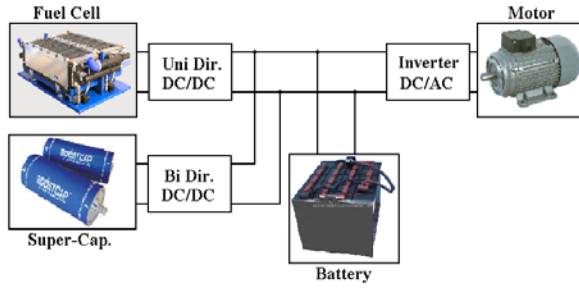


Fig. 1. FC-SC-Battery hybrid system.

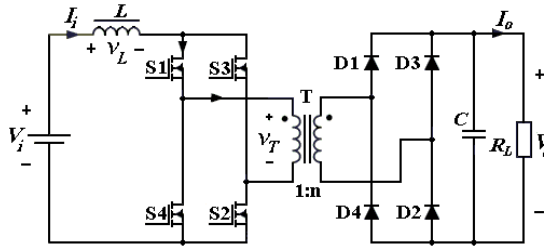


Fig. 2. Isolated full-bridge DC/DC converter.

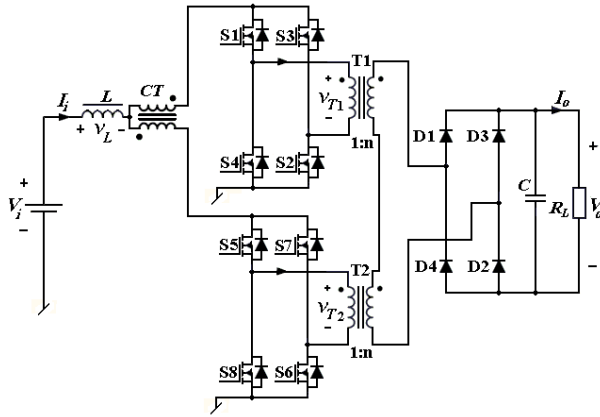


Fig. 3. High-efficiency isolated DC/DC converter.

2. Bi-directional DC/DC Converter

Fig. 4 shows the modified bidirectional DC/DC converter suitable for SC applications. The converter includes two full-bridge stages in the primary side and one in the secondary. Two inductors with the same value ($L_1=L_2$) are used as boosting elements. This configuration (TSI) can eliminate the requirement of current balancing transformer (CBT). The proposed converter has two discharging and charging operating states. These states will be illustrated in the following sections.

Fig. 5 shows the gate signals for the proposed converter. The gate signals for the primary side switches are similar to the original unidirectional configuration. The gate signals for the secondary side can be produced using the logical NOT of the gate signals of primary side switches. However to avoid short-circuit in the output, dead-time should be considered.

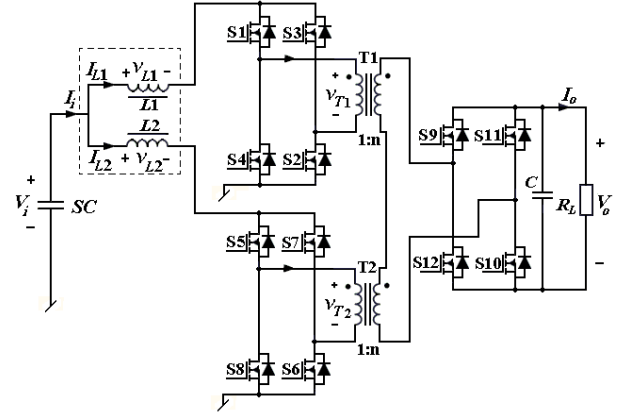


Fig. 4. Proposed isolated bi-directional DC/DC converter.

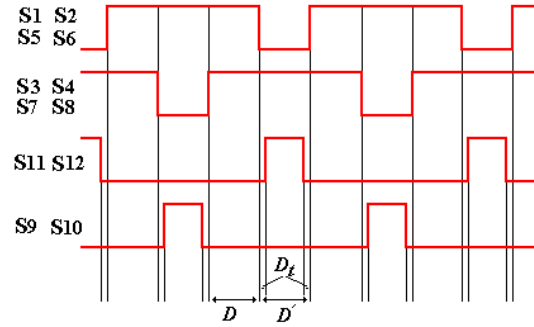


Fig. 5. Gate signals of proposed converters.

3. Normal Operation Modes

The proposed converter can be simply modeled as in Fig. 6a and Fig. 6b for CBT and TSI configurations, respectively. Upper and lower primary full-bridge stages have been modeled by switches S_{m1} and S_{m2} and secondary side bridge has been modeled by S_m .

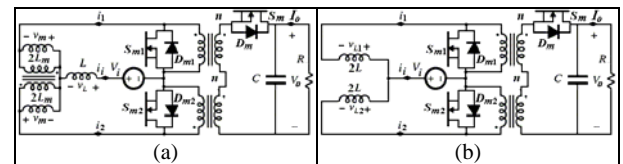


Fig. 6. Simplified models of the proposed converter: a) CBT b) TSI.

A. Discharge state

In the discharging of SC, secondary inverter acts as a rectifier. Considering the gate signals in Fig. 5, two main operation modes can be defined for the converter.

1) Mode 1: Both S_{M1} and S_{M2} are closed and inductors are charging (Fig. 7).

2) Mode 2: Both S_{M1} and S_{M2} are open and inductors are discharging (Fig. 8).

Fig. 9 shows waveforms for both inductor configurations. For the same ripple current, inductance of each inductors of TSI topology is twice of inductance of inductor of CBT topology. It can be proven for both cases that:

$$V_o = \frac{2n}{D'} V_i \quad (1)$$

$$I_i = \frac{2n}{D'} I_o \quad (2)$$

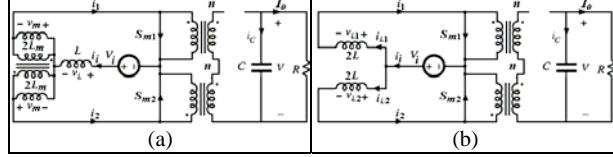


Fig. 7. Model of proposed converters in mode 1 of of discharging state: a) CBT b) TSI.

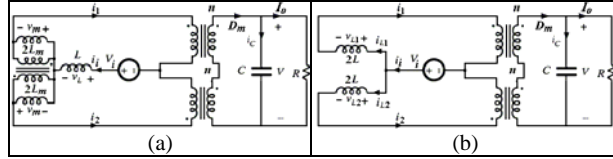


Fig. 8. Model of proposed converters in mode 2 of of discharging state: a) CBT b) TSI.

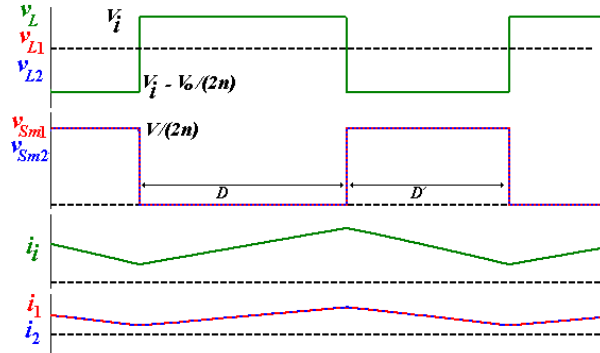


Fig. 9. Waveforms of proposed converters in normal modes of discharging state.

B. Charge state

Primary side full-bridge stages act as rectifiers during the charging of the SC. Similar to the discharge state, two main operation modes can be considered in the charge state.

1) Mode 1: Both diodes D_{M1} and D_{M2} are conducting and inductors (or inductor) are discharging (Fig. 10).

2) Mode 2: Both D_{M1} and D_{M2} are open and inductors (or inductor) are charging (Fig. 11).

Fig. 12 shows waveforms for both configurations. Neglecting deadtime, it can be proved that (1) and (2) are correct in the charge state. However considering deadtime (Fig. 5), it can be written:

$$V_o = \frac{2n}{D' - 2D_t} V_i \quad (3)$$

$$I_i = \frac{2n}{D' - 2D_t} I_o \quad (4)$$

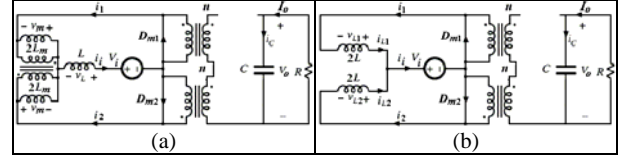


Fig. 10. Model of proposed converters in mode 1 of of charging state: a) CBT b) TSI.

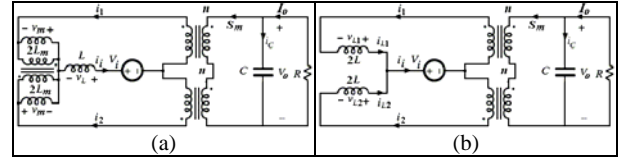


Fig. 11. Model of proposed converters in mode 2 of of charging state: a) CBT b) TSI.

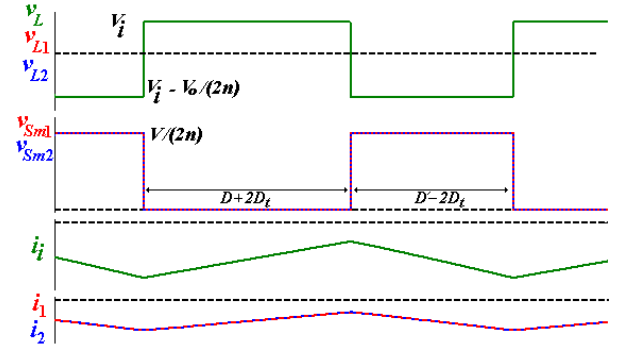


Fig. 12. Waveforms of proposed converters in normal modes of charging state.

4. Extra Operation Modes

In the previous section, it is assumed that both primary side switches are switched at same time. However it is possible that this assumption is not satisfied because of differences in component propagation delays and parasitic elements. Considering a single boosting inductor (without CBT), when one of the simplified switches in Fig. 6 is ON, all the current i_i passes through the corresponding bridge. However in CBT and TSI configurations, due to the high impedance seen between the two full-bridge structures, the current going through each full-bridge can not change instantaneously. Therefore either CBT or TSI should be included in the topology for proper current balancing.

A. Discharge state

Two extra modes can be defined in discharge state:

1) Mode 3 (Fig. 13): S_{M1} is ON and S_{M2} is OFF. When both modeling switches S_{M1} and S_{M2} are OFF, but $i_2 > i_1$, this mode also can be created as diode D_{M1} is ON.

2) Mode 4 (Fig. 14): S_{M1} is OFF and S_{M2} is ON. When both modeling switches S_{M1} and S_{M2} are OFF, but $i_1 > i_2$, this mode also can be created as diode D_{M2} is ON.

Extra modes 3 and 4 are generally similar to mode 2; however currents i_1 and i_2 are not equal in these modes. As an example, assuming a delay for turning S_{M1} ON, we will investigate the operation of the converter in both CBT and TSI configurations.

1) CBT: When both switches S_{M1} and S_{M2} are OFF, converter is in mode 2. When S_{M1} turns ON and S_{M2} is OFF (due to signaling mismatch), converter has mode 3 (Fig. 13-a). In this mode CBT sees a non-zero voltage; therefore magnetizing current of CBT is increasing. This current creates a current difference between two primary inverters. With turning S_{M2} ON, converter goes to mode 1, while there is a constant current difference between two primary bridges. When both switches turn OFF again (it is assumed no delay for turning OFF), converter can not enter to mode 2 because inverter currents are not same. Since i_1 is greater than i_2 , simplified diode D_{M2} is forced to be ON and thus converter will be in mode 4. During mode 4, CBT sees negative non-zero voltage and magnetizing current of CBT is decreasing. Mode 4 continues until both inverter currents are the same. The required time interval for this is equal to the switching delay time interval between the two switches, S_{M1} and S_{M2} . After mode 4, converter goes to mode 2. Fig. 15-a shows the waveforms of CBT topology in this condition.

2) TSI: When both switches S_{M1} and S_{M2} are OFF, converter is in mode 2. When S_{M1} turns ON and S_{M2} are OFF (due to signaling mismatch), converter is in mode 3 (Fig. 13-b). In this mode inductors see different voltages; L_1 sees positive voltage and L_2 sees negative voltage. This creates a current difference between the two inductors. With S_{M2} turning ON, converter goes to mode 1, while there is a constant current difference between the two primary bridges. When both turn OFF again, converter can not enter to mode 2 because inverter currents are not same. Since i_1 is greater than i_2 , simplified diode D_{M2} is forced to be ON and thus converter will be in mode 4. During mode 4, L_1 sees negative voltage and L_2 sees positive voltage. Therefore the current difference between the inductors is decreasing. Mode 4 continues until both bridge currents are the same. The required time interval for this is equal to the switching delay time interval between the two switches, S_{M1} and S_{M2} . After mode 4, converter goes to mode 2. Fig. 15-b shows the waveforms of TSI topology in this condition. For both configurations, it can be proven that:

$$V_o = \frac{2n}{D' + d_{on}} V_i \quad (5)$$

$$I_i = \frac{2n}{D' + d_{on}} I_o \quad (6)$$

For both configurations, it can be seen from Fig. 15 that average of currents i_1 and i_2 are not equaled when there is a switching delay. Defining current difference I_d as:

$$I_d = 0.5(I_1 - I_2) \quad (7)$$

Therefore for CBT and TSI configurations it can be written that:

$$I_{d-CBT} = \frac{D d_{on} V_i}{2 f_{SW} L_m (D' + d_{on})} \quad (8)$$

$$I_{d-TSI} = \frac{D d_{on} V_i}{2 f_{SW} L (D' + d_{on})} \quad (9)$$

Therefore for $L=L_m$, both configurations have similar current difference. However assuming d_{on} is small, current difference (I_d) is small in both configurations.

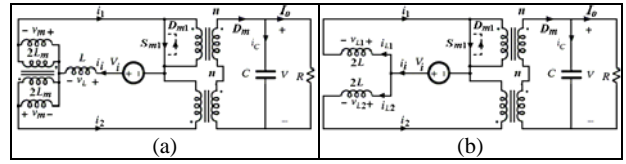


Fig. 13. Model of proposed converters in mode 3 of discharging state: a) CBT b) TSI.

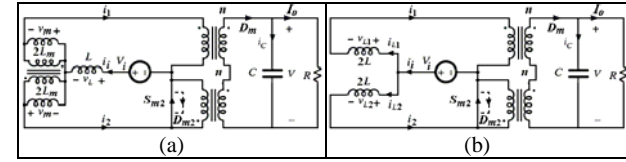


Fig. 14. Model of proposed converters in mode 4 of discharging state: a) CBT b) TSI.

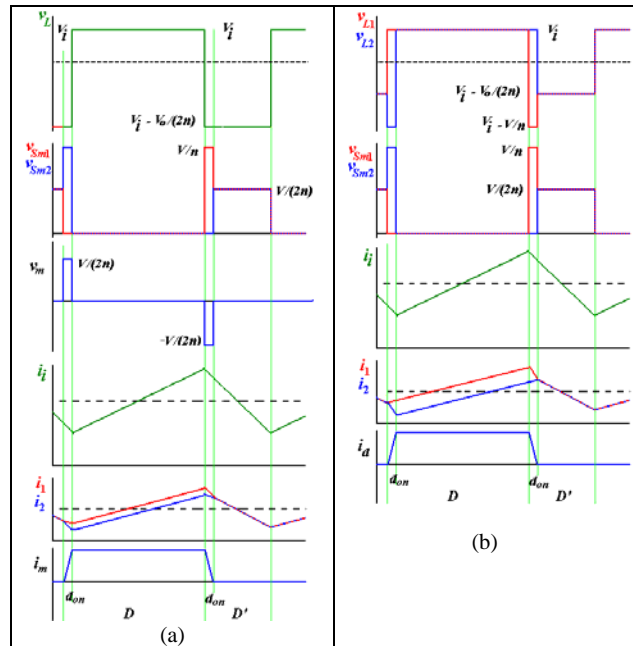


Fig. 15. Waveforms of proposed converters in discharging state with switching delay.

B. Charge state

Similar to discharge state, two extra modes can be defined for charge state:

- 1) Mode 3 (Fig. 16): D_{M1} (or S_{M1}) is ON and D_{M2} is OFF.
- 2) Mode 4 (Fig. 17): D_{M1} is OFF and D_{M2} (or S_{M2}) is ON.

Similar to discharge state, extra modes 3 and 4 are generally similar to mode 2; however currents i_1 and i_2 are not equal in these modes. In charge state, if switching delays are smaller than the deadtime, extra modes does not occur. However, extra modes can occur due to the difference between inductor values.

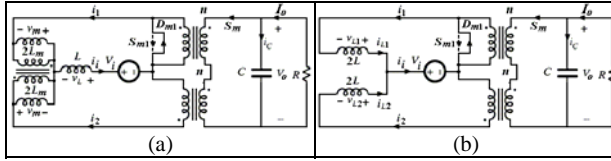


Fig. 16. Model of proposed converters in mode 3 of of charging state: a) CBT b) TSI.

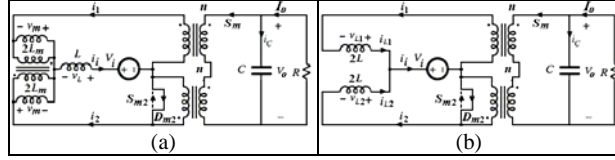


Fig. 17. Model of proposed converters in mode 4 of of charging state: a) CBT b) TSI.

5. Control System

A hybrid system such as in Fig. 1 usually includes a central control system (CCS) and several local control subsystems (Fig. 17-18). Regarding system condition, CCS determines references signals for the control subsystems.

In this paper, it is assumed that the reference current of SC is determined by CCS. Fig. 19 shows the control subsystem for the proposed converter for an SC application. Control system is based on average current mode control. A PI controller has been used for ensuring the steady state current error to be zero. Output of PI is a duty cycle value that it is applied to the PWM block.

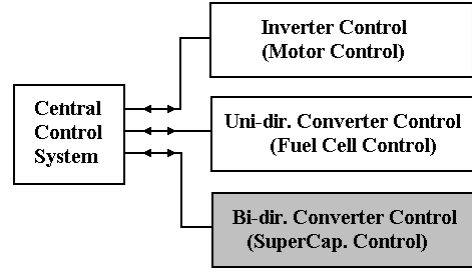


Fig. 17. Control system of FC-SC-Battery hybrid system (Fig. 1).

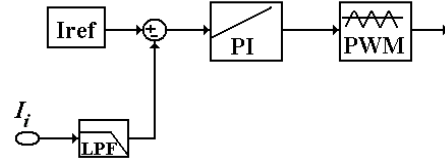


Fig. 18. Control subsystem of proposed bidirectional converter (supercapacitor).

6. Simulation and Experiments

To verify the proposed converter, the control system shown in Fig. 18 was used for simulation. Table 1 shows simulation parameters. A step signal has been used as the reference current; it changes from -100A to 100A at $t=2\text{ms}$. Before $t=2\text{ms}$ the converter will be in the charge state and after that converter should go to the discharge state. Simulation results have been shown in Fig. 19. It can be seen that the SC current (I_i) is able to track the reference current signal in both charge and discharge states.

An experimental prototype of the modified bidirectional DC-DC converter was also built using digital signal processor (DSP) control (Fig. 20). Experimental current I_i has been shown in Fig. 21. It could be observed that the closed loop controller implemented in the DSP is able to make the SC current follow the reference current in a stable manner. Implementation of the two transformers with primary parallel and secondary series connection is realized with planar E-type cores.

Table I. - Simulation Parameters

Parameters	Values
Capacitance of SC	100 F
Internal resistance of SC	0.005
Initial voltage of SC	25 V
Battery voltage (V_b)	80 V
Proportional gain (K_p) of PI	0.001
Integral gain (K_i) of PI	10
Transformer turns ratio (n)	3
Inductors	10 μH
Switching Frequency	50 kHz

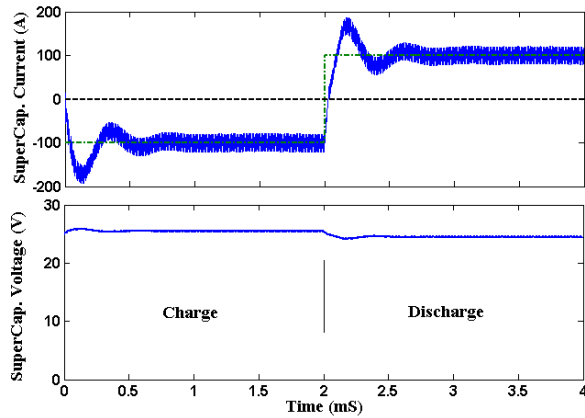


Fig. 19. Simulation results: Current and voltage of supercapacitor.

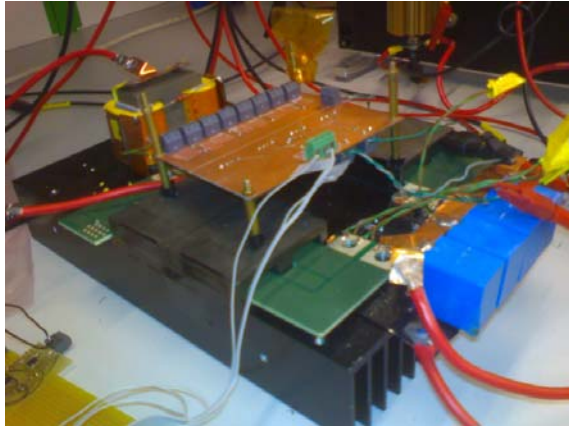


Fig. 20. Experimental prototype of the proposed converter.

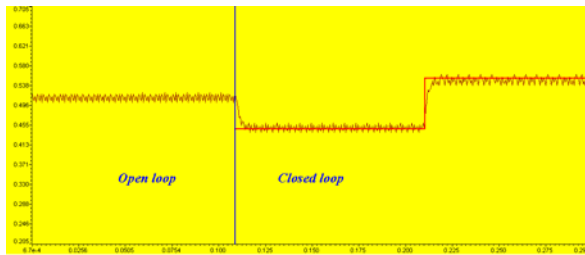


Fig. 21. Experimental results: Reference and (filtered) actual current (sampled by the DSP).

7. Conclusion

In this paper an isolated bidirectional DC/DC converter has been proposed for supercapacitor applications. The proposed converter uses a parallel structure on the primary side (low-voltage high-current side) and a series structure in secondary side (high-voltage low-current side). This structure has already been used for unidirectional power flow in the literature. It has been developed for bidirectional power flow in this paper. Also a new method was proposed for current sharing on the primary side. In the proposed method, separate inductors can be used instead of the conventional method which is based on a current balancing transformer. Although both methods

have almost the same performance, manufacturing the separate inductors is easier. However, inductance value tolerances can be a problem in fair current distribution between the parallel primary stages. Performance and validity of the proposed converter has been verified by simulation and experimental results.

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